

CURRICULUM VITAE



Name: Dr. PRAVEEN CHITTI

Date of Birth: 16th June 1979

Vision:

To obtain a challenging and prestigious role in a renowned educational institution, where I can utilize and enhance my expertise to inspire and empower students, contributing to the growth of the institution, society, and my own professional development.

Education Details:

Sl. No.	Exam/Degree	Board/University	Year of Passing	Class
1.	Ph. D.	VTU Belagavi	2023	-----
2.	M. Tech. (VLSI)	VTU Belagavi	2005	First class With Distinction
3.	B. E. (E&C)	Karnataka University Dharwad	2001	First class With Distinction
4.	P. U. C.	PUC Board Bangalore	1997	First class
5.	S.S.L.C.	KSEEB Bangalore	1995	First class With Distinction

Teaching Experience: 23.7 Years (Research: 9 Years)

- 1) Currently working as **Professor and Principal**, at Maratha Mandal's Engineering College Belagavi from March-2025.
- 2) Worked as **Professor** in the Dept of Electronics & Communication Engineering and **Dean Examinations & University Affairs**, at Jain College of Engineering Belagavi from May-2023 to February-2025
- 3) Worked as **Dean Academics & Examinations**, at Jain College of Engineering Belagavi from August-2017 to April-2023.
- 4) Worked as an **Assistant Professor and Head** of Dept of Computer Science and Engineering, JCE Belagavi from July 2014 to October 2020.
- 5) Worked as an **Assistant Professor and Head** of Dept of Electronics & Communication Engineering, JCE Belagavi from July 2012 to July 2014.
- 6) Worked as an **Assistant Professor and Head** of Dept of MCA, JCE Belagavi from August 2011 to July 2012.
- 7) Worked as an **Assistant Professor** in the Dept of Electronics & Communication Engineering, JCE Belagavi from August 2010 to August 2011.
- 8) Worked as an **Assistant Professor** in the Dept of Electronics & Communication Engineering, KLESCET Belagavi from June 2007 to August 2010.

- 9) Worked as a **Lecturer** in the Dept of Electronics & Communication Engineering, KLESCET Belagavi from 2001 to 2007.

Publications:

- 1) Shahapur SS, Chitti P, Patil S, Nerurkar CA, Shivannagol VS, Rayanaikar VC, Sawant V, Betageri V (2024) **Decoding Minds: Estimation of Stress Level in Students using Machine Learning**. Indian Journal of Science and Technology 17(19): 2002-2012. <https://doi.org/10.17485/IJST/v17i19.2951>, 2024.
- 2) Praveen Chitti, Dr. K. Prabhushetty, **A Deep Learning and Machine Learning Approach for Image Classification of Tempered Images in Digital Forensic Analysis**. (IJACSA) International Journal of Advanced Computer Science and Applications, Vol. 13, No. 10, 2022.
- 3) Praveen Chitti, Dr. K. Prabhushetty. (2020), **“A Secured Privacy Preserving CBIR Using Color Co-Occurrence Matrix And Gabor Wavelet Transformation”**. International Journal of Advanced Science and Technology, 29(06), 2020, 5756 - 5764.
- 4) Published a paper titled **“Implementation of Driver Vigilance System using Deep Learning and Advance Computer Vision”**, in International Journal of Engineering Research & Technology (IJERT) special issue-2020, ISSN: 2278-0181.
- 5) Praveen Chitti, K. Prabhushetty, **“Secured CBIR with Anonymity Preserving for Images and Users in Cloud Environment”**, IEEE Sponsored 2019, 5th International Conference For Convergence In Technology, Pune (MS), INDIA, 29th -31st March 2019 (Publication in IEEE Xplorer).
- 6) Praveen Chitti, K. Prabhushetty, **“Investigation of Image Forensic Techniques to Determine Faked Images”**, World Journal of Technology, Engineering and Research, Volume 2, Issue 1 (2017) 414-420.
- 7) Presented a paper entitled **“Implementation of Sign Language Tutor Using FPGA”** in the international conference held at VIT Pune Maharashtra.
- 8) Presented a paper entitled **“Software Controlled Irrigation Management System”** in the national conference held at PIMR Indore Madhya Pradesh.
- 9) Presented a paper entitled **“Exploring and Implementing a MAC transmitter for transmission of UDP packets within FPGA”** at the international conference on Information & Communication Engineering (ICICE-2013) held at Bangalore, Karnataka.

Additional Responsibilities Handled:

- 1) Working as **Member Board of Studies (BOS)** in Electrical Engineering Sciences (EC/EEE/TE/BM/ML/IE and allied programs) of **VTU Belagavi**, for the years 2022-2025.

- 2) Worked as **Member Board of Examiners (B.O.E.)** in **EC/TC board** of **VTU Belagavi** for the academic year 2023-2024.
- 3) Worked as **Member Board of Examiners (B.O.E.)** in **CSE Board** of **VTU Belagavi** for the academic year 2020-2021.
- 4) Handled the **Administrative Responsibilities** as a head of three departments i.e. **CSE, MCA and ECE** at JCE Belagavi.
- 5) Worked as a **convener** for the International Conference **ICETES-2015** organized at JCE Belagavi.
- 6) Worked as an **Organizing Chair and Convener** for the International Conference **INCET-2020** organized at JCE Belagavi, sponsored by IEEE Bangalore section.
- 7) Actively involved in all **College** and **University** activities.
- 8) Looking after the **routine administrative work** of the college as in-charge in the absence of principal at JCE Belagavi.
- 9) Worked as **Reviewer** for many IEEE sponsored International Conferences.
- 10) Worked as **VTU e-Shikshana** Coordinator.
- 11) Working as **IIT Bombay Spoken Tutorial** remote centre Coordinator.
- 12) Working as **ISRO-IIRS e-learning** Coordinator.
- 13) Worked as **Institute NIRF** coordinator.
- 14) Worked as **Institute NAAC** coordinator.
- 15) Worked as **VTU Nominee** for staff selection process.
- 16) Approved **PhD Research Supervisor** from VTU Belagavi.
- 17) Worked as **VTU Valuation** Coordinator for 5 years.
- 18) Working as a Department **Academic Advisory Committee** member at Electronics & Telecommunication department of **SGMCOE Mahagaon**, Maharashtra.
- 19) Working as **NTA observer** for Examinations like NEET, JEE, AISSEE and UGC NET for last 14 years.
- 20) Worked as **Centre Superintendent** for examinations of NEET, KPSC, HESCOM, GATE and Police recruitment exam etc.

QIP Short Term Courses attended:

- 1) Attended one week QIP Short Term Course on Electromagnetics at IISc Bangalore.
- 2) Attended one week QIP Short Term Course on EMBEDDED SYSTEM DESIGN at IISc Bangalore.
- 3) Attended One week AICTE sponsored ATAL FDP on IoT at IIIT, Nagpur.
- 4) Attended One week AICTE sponsored ATAL FDP on Artificial Intelligence at IIIT, Nagpur.

- 5) Attended One week AICTE sponsored ATAL FDP on Signal Processing using FPGA at KLE Technological University, Hubballi.
- 6) Attended One week AICTE sponsored ATAL FDP on Implementation of National Education Policy-2020 at ATME College of Engineering, Mysuru.
- 7) Attended one month Master Class on Artificial Intelligence conducted by Pantech e-learning, Chennai.

Workshops and Conferences Attended:

- 1) Attended 2 days National Workshop on Embedded Systems & Energy Management at KLESCET Belagavi.
- 2) Attended 4 days Self and Career Development Program conducted by Indian Heritage Academy Bangalore.
- 3) Attended 5 days faculty training program on VLSI Design with CADENCE Tools at VTU Belagavi.
- 4) Attended a workshop on digital design using VHDL at BVBCET Hubli.
- 5) Attended a workshop on digital design with Verilog HDL at KLESCET Belagavi.
- 6) Attended one week workshop on Examination Reforms conducted by AICTE.

Invited Lectures Delivered:

- 1) Worked as a Guest Faculty to teach **B. Tech.** students of **Robotics & Automation** at VTU Belagavi for the academic year 2021-2022.
- 2) Delivered a lecture on Microcontroller Programming Concepts at **STTP** held at Motichand Lengade Bharatesh Polytechnic Belagavi.
- 3) Delivered a lecture on Fundamentals of Electronics at the workshop for High school science Teachers conducted by **BEO Belagavi**.
- 4) Worked as a visiting faculty to teach **M. Tech.** students at department of Post Graduate studies at VTU Belagavi for 5 years.
- 5) Delivered lectures on Network Analysis subject for **VTU EDUSAT** program.

Awards and Achievements:

- 1) Received Best paper award for the paper “**Investigation of Image Forensic Techniques to Determine Faked Images**”, World Journal of Technology, Engineering and Research, Volume 2, Issue 1 (2017) 414-420.
- 2) Received **Outstanding Education Award-2024** from Gramin Sahitya Sangha Yellur, Karnataka.

Number of PhD Candidates Guiding:

Faculty of E&C Engineering: 01

Number of projects guided:

Post Graduate: 26

Under Graduate: 61

Member of Professional Bodies:

Institute of Electrical and Electronics Engineers (IEEE)

Computer Society of India (CSI)

Subjects Handled:**For Under Graduate Students**

Information and Network Security, Fundamentals of HDL, Digital Systems Design Using VHDL, Basic Electronics, Analog Communication, Field Theory, Microprocessors, Electrical and Electronic Measurements, Electronic Circuits, Analog Electronic Circuits, Power Electronics, Electronic Instrumentation, Advanced Microprocessors, Linear Integrated Circuits, Analog and Mixed Mode VLSI Design, Computer Concepts and C Programming, Logic Design, Network Analysis, Control Systems, Analog and Digital Electronics, Programming in C and Data Structures, Microprocessors and Microcontrollers, Microcontrollers and Embedded systems, Basic Electronics & Communication Engineering, Microcontrollers, Introduction to Electronics Engineering etc.

For Post Graduate Students

Advanced Embedded Systems, Digital Systems Design Using VHDL, Testing and Verification of VLSI circuits, Synthesis and Optimization of Digital Circuits, Hardware Software Co Design, CMOS RF Circuit Design, Advances in VLSI Design and Algorithms, Object Oriented Programming Using C++, Advanced Computer Networks, Analog and Mixed Mode VLSI Design etc.

Contact Information:**Dr. Praveen Chitti**

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https://scholar.google.com/citations?user=KRvv_dsAAAAJ

AICTE Faculty Id: **1-459843121**

VTU Faculty Id: **2JICS0001827**

VTU Research Supervisor Id: **042024RSEC005055**